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Java as a specification language for hardware-software systems

Rachid Helaihel, Kunle Olukotun

November 1997 I CCAD '97: Proceedings of the 1997 I EEE/ACM international conference o aided design

Publisher: IEEE Computer Society

Full text available: Publisher Site, Pdf (46.54 KB) Additional Information: full citation, abstract, referen-index terms

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 16, Citation Count: 28

The specification language is a critical component of the hardware-software co-design is used for functional validation and as a starting point for hardware- software partitior synthesis. This paper pro poses the Java programming ...

Keywords: java, specification languages, hardware-software co-design

² An experience teaching operating systems course with a programming project Suban Krishnamoorthy

May 2002 Journal of Computing Sciences in Colleges, Volume 17 Issue 6

Publisher: Consortium for Computing Sciences in Colleges

Full text available: Pdf (58.90 KB)

Additional Information: full citation, abstract, references, index to

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 74, Citation Count: 0

An operating system course can be taught with or without a programming project. The found that teaching it with a programming project has several advantages. In particula gain the valuable implementation experience of the essential ...

3 A self-maintained memory module supporting DMM

Weixing Ji, Feng Shi, Baojun Qiao

September 2007 CASES '07: Proceedings of the 2007 international conference on Compile architecture, and synthesis for embedded systems

Publisher: ACM

Full text available: Pdf (456.36 KB)

Additional Information: full citation, abstract, references, index to

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 36, Citation Count: 0

The memory intensive nature of object-oriented languages such as C_{++} and Java has need of a high-performance dynamic memory management (DMM); however, it is a ch to provide efficient reliable system without violating real time ...

Keywords: active memory module, dynamic memory management, object-based cacl oriented programming

4

OSEK/VDX API for Java

Michael Stilkerich, Christian Wawersich, Andreas Gal, Wolfgang Schröder-Preikschat, Michael Schröder-Preikschat,

Publisher: ACM

Full text available: Pdf (148.90 KB) Additional Information: full citation, abstract, references, index to

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 55, Citation Count: 1

Modern cars contain a multitude of micro controllers for a wide area of tasks. The diverneterogeneous hardware and software leads to a complicated and expensive integratio process. Integrating multiple tasks on fewer micro controllers reduces ...

⁵ ARCS: an architectural level communication driven simulator

Dave Nellans, Vamshi Krishna Kadaru, Erik Brunvand

April 2004 GLSVLSI '04: Proceedings of the 14th ACM Great Lakes symposium on VLSI

Publisher: ACM

Full text available: Pdf (138.92 KB) Additional Information: full citation, abstract, references, index to

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 15, Citation Count: 0

Simulators for digital systems operate at a variety of levels of abstraction varying from analog and switch level modeling of the transistor to cycle based descriptions of entire propose an even higher level simulator, called ARCS, ...

Keywords: Java, architectural simulation, asynchronous communication

6 Bulk Disambiguation of Speculative Threads in Multiprocessors

Luis Ceze, James Tuck, Josep Torrellas, Calin Cascaval

June 2006 ISCA '06: Proceedings of the 33rd annual international symposium on Compu Architecture

Publisher: ACM

Full text available: Pdf (376.15 KB)

Additional Information: full citation, abstract, references, cited by

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 137, Citation Count: 6

Transactional Memory (TM), Thread-Level Speculation (TLS), and Checkpointed multip three popular architectural techniques based on the execution of multiple, cooperating threads. In these environments, correctly maintaining data ...

Also published in:

May 2006 SIGARCH Computer Architecture News Volume 34 Issue 2

7 Emulation with DSOL

Peter H. M. Jacobs, Alexander Verbraeck, William Rengelink

December 2005 WSC '05: Proceedings of the 37th conference on Winter simulation

Publisher: Winter Simulation Conference

Full text available: Pdf (484.32 KB) Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 28, Citation Count: 1

Manufacturing control systems are extremely hard to design and test. Testing Program Controller (PLC) software in an on-line manufacturing setting can be costly, dangerous inefficient. The availability of a seamless transition between ...

8 Experiences in code migration from main applications to J2EE servers

Pat Shuff, Michael Bolton

June 2004 ISICT '04: Proceedings of the 2004 international symposium on Information a communication technologies

Publisher: Trinity College Dublin

Full text available: Pdf (93.45 KB)

Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 17, Citation Count: 0

From July through December of 2003, we migrated and stressed tested a Java progran web authentication for a user community of about fifty thousand students, staff, and fainitially started with some simple Java code and migrated ...

The OSIRIS-SE (stream-enabled) infrastructure for reliable data stream managem
 devices

Gert Brettlecker, Heiko Schuldt

June 2007 SI GMOD '07: Proceedings of the 2007 ACM SIGMOD international conference

Management of data

Publisher: ACM

Full text available: Pdf (862.08 KB)

Additional Information: full citation, abstract, references, index to

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 129, Citation Count: 0

The proliferation of software and hardware sensors which continuously create large am has significantly facilitated novel types of applications such as healthcare telemonitorin traffic management. All these applications demand ...

Keywords: checkpointing, data stream management, healthcare, information manage infrastructure, telemonitoring

10 The COSIMA prototype for multi-objective bargaining

Stefan Fischer, Werner Kießling, Stefan Holland, Michael Fleder

July 2002 AAMAS '02: Proceedings of the first international joint conference on Autonor and multiagent systems: part 3

Publisher: ACM

Full text available: Pdf (385.79 KB) Additional Information: full citation, abstract, references, cited by

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 23, Citation Count: 3

Multi-objective bargaining is a challenging business model for B2C or B2B Internet app visual bargaining agent COSIMA builds on cooperative database technology like Prefere Preference XPATH, which can efficiently implement \linebreak[4] ...

Keywords: bargaining strategies, cooperative, multi-objective bargaining

11 Runtime integrity checking for inter-object connections

Guilin Chen, Mahmut Kandemir

May 2005 | I CCAD '05: Proceedings of the 2005 | IEEE/ ACM International conference on Codesign

Publisher: IEEE Computer Society

Full text available: Pdf (222.31 KB) Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 10, Citation Count: 0

Ensuring integrity of heap resident data is critical for many embedded systems. Ever-s technology combined with power-saving techniques employed in embedded systems is vulnerability of such systems to hardware-related errors ...

12 Design and development of computer networking modules using virtual instrument

oriented programming

Nikunja K. Swain, Mrutyunjaya Swain

October 2004 CITC5 '04: Proceedings of the 5th conference on Information technology ed

Publisher: ACM

Full text available: Pdf (95.75 KB)

Additional Information: full citation, abstract, index terms

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 38, Citation Count: 0

The students' over reliance upon formulas and routine use of technique in problem solve lead to poor performance in advanced courses and a high attrition rate in the engineer technology, and science programs. The students' lack of comprehension ...

Keywords: IP classes, TCP/IP, dotted decimal notation, java/visual basic, router forwatable, subnet & subnet mask, virtual instruments

13 The national ignition facility: early operational experience with a large Ada control

Robert W. Carey, Paul J. Van Arsdall, John P. Woodruff

December 2002 SI GAda '02: Proceedings of the 2002 annual ACM SI GAda international contract Ada: The engineering of correct and reliable software for real-time & distribution using Ada and related technologies

Publisher: ACM

Full text available: Pdf (120.37 KB) Additional Information: full citation, abstract, cited by, index term

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 28, Citation Count: 1

The National Ignition Facility (NIF) currently under construction at the University of Ca Lawrence Livermore National Laboratory (LLNL) is a 192-beam, 1.8-Megajoule, 500-Te being built by the Department of Energy and the National ...

Also published in:

March 2003 SIGAda Ada Letters Volume XXIII Issue 1

14 Database patchwork on the Internet

Reinhard Braumandi, Alfons Kemper, Donald Kossmann

June 1999 SI GMOD '99: Proceedings of the 1999 ACM SIGMOD international conference Management of data

Publisher: ACM

Full text available: Pdf (374.70 KB)

Additional Information: full citation, abstract, references, cited by

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 22, Citation Count: 3

Naturally, data processing requires three kinds of resources: the data itself, the functic database operations) and the machines on which to run the operations. Because of the

Also published in:

June 1999 SIGMOD Record Volume 28 Issue 2

15 The future of system design languages (panel session)

Clifford E. Cummings, Steven E. Schulz, Simon Davidman, John Sanguinetti, Joachim Kun Richard Goering

June 2000 DAC '00: Proceedings of the 37th conference on Design automation

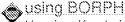
Publisher: ACM

Additional Information: full citation, abstract, cited by, index terms

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 1

Verilog HDL was a breakthrough for the hardware design community in 1986. Over the methodology based on the Verilog HDL has been extended with utilities and enhancem 0.25- and 0.18 μ processes enabling a system to be packed ...

16 A unified hardware/software runtime environment for FPGA-based reconfigurable



Hayden Kwok-Hay So, Robert Brodersen

February 2008 Transactions on Embedded Computing Systems (TECS), Volume 7 Issue

Publisher: ACM

Full text available: Pdf (646.00 KB) Additional Information: full citation, abstract, references, index to

Bibliometrics: Downloads (6 Weeks): 29, Downloads (12 Months): 230, Citation Count: 0

This paper explores the design and implementation of BORPH, an operating system de FPGA-based reconfigurable computers. Hardware designs execute as normal UNIX proc BORPH, having access to standard OS services, such as file system ...

Keywords: BORPH, FPGA, hardware process, reconfigurable computers

17 Deconstructing process isolation

Mark Aiken, Manuel Fähndrich, Chris Hawblitzel, Galen Hunt, James Larus Mark Aiken, Manuel Fähndrich, Chris Hawblitzel, Galen Hunt, James Larus

October 2006 MSPC '06: Proceedings of the 2006 workshop on Memory system performar correctness

Publisher: ACM

Full text available: Pdf (225.75 KB) Additional Information: full citation, abstract, references, index to

Bibliometrics: Downloads (6 Weeks): 19, Downloads (12 Months): 95, Citation Count: 3

Most operating systems enforce process isolation through hardware protection mechan memory segmentation, page mapping, and differentiated user and kernel instructions. new operating system that uses software mechanisms to ...

Keywords: hardware isolated process (HIP), hardware protection domain, singularity, isolated process (SIP)

18 A hardware/software prototyping environment for dynamically reconfigurable embesystems

Josef Fleischmann, Klaus Buchenrieder, Rainer Kress

March 1998 CODES/ CASHE '98: Proceedings of the 6th international workshop on Hardw codesign

Publisher: IEEE Computer Society

Full text available: Pdf (42.66 KB)

Additional Information: full citation, references, cited by, index te

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 15, Citation Count: 5

19 Developing mobile 3D applications with OpenGL ES and M3G

Kari Pulli, Jani Vaarala, Ville Miettinen, Tomi Aarnio, Mark Callow

July 2005 SIGGRAPH '05: SIGGRAPH 2005 Courses

Publisher: ACM

Full text available: Pdf (9.22 MB) Additional Information: full citation

Bibliometrics: Downloads (6 Weeks): 26, Downloads (12 Months): 311, Citation Count: 0

20 XTREM: a power simulator for the Intel XScale® core

👞 Gilberto Contreras, Margaret Martonosi, Jinzhan Peng, Roy Ju, Guei-Yuan Lueh

July 2004 LCTES '04: Proceedings of the 2004 ACM SIGPLAN/SIGBED conference on Lar compilers, and tools for embedded systems

Publisher: ACM

Full text available: Pdf (1.07 MB) Additional Information: full citation, abstract, references, cited by

Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 103, Citation Count: 15

Managing power concerns in icroprocessors has become a pressing research problem a domains of computer architecture, CAD, and compilers. As a result, several parameter power simulators have been introduced. While these simulators ...

Keywords: Java, XORP, XScale, hardware performance counters, power measuremen modeling

Also published in:

July 2004 SIGPLAN Notices Volume 39 Issue 7

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